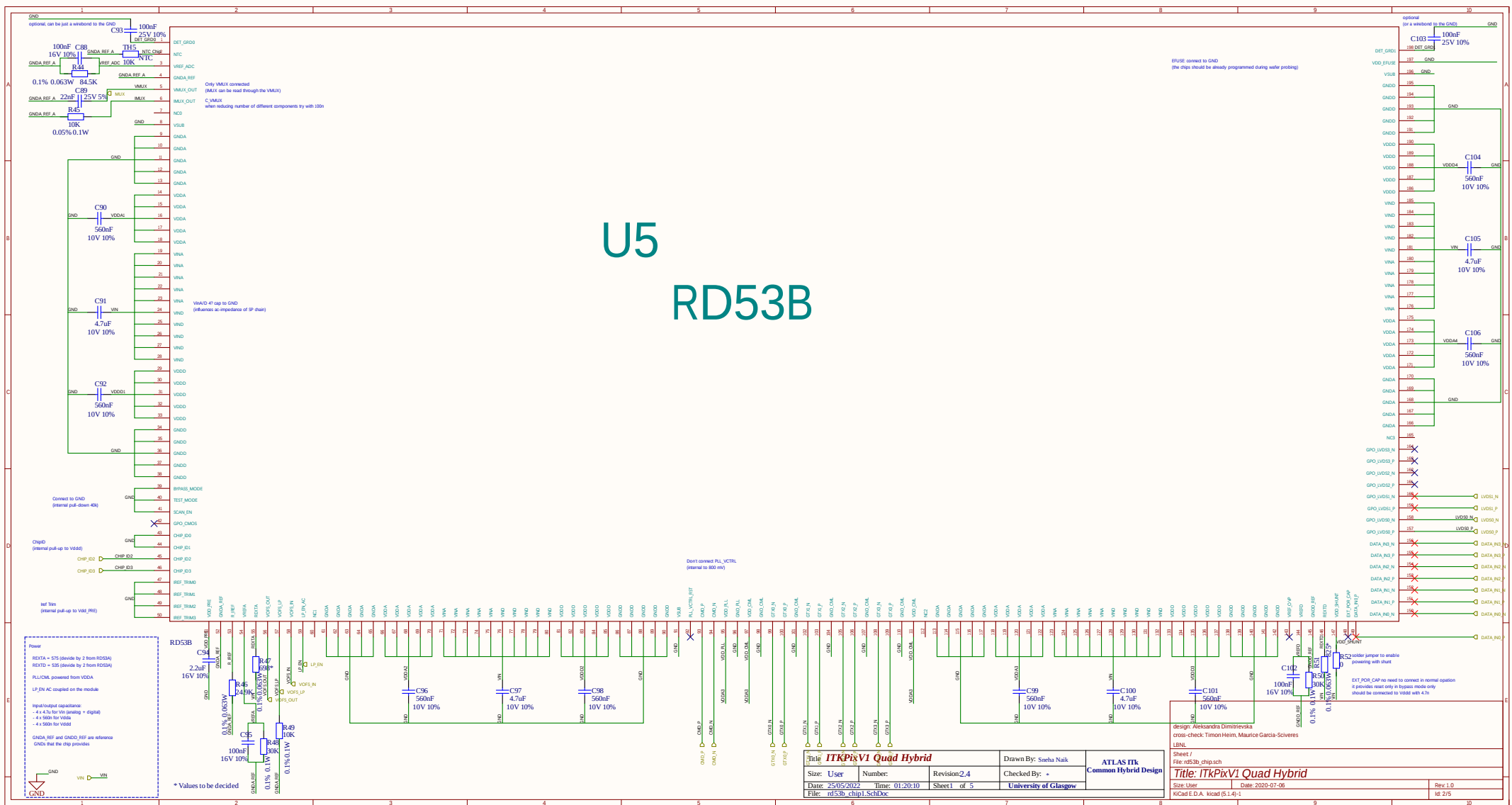
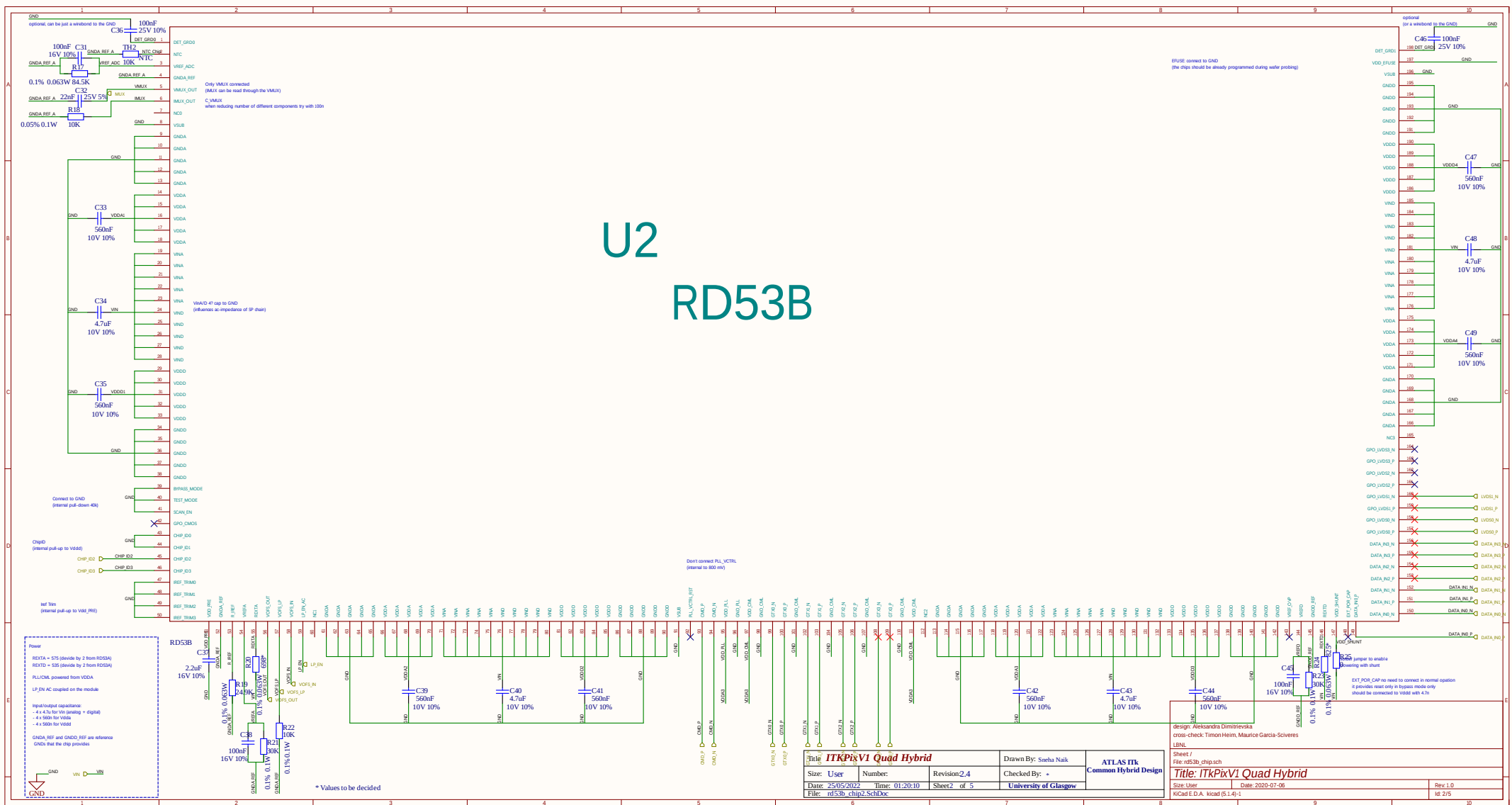
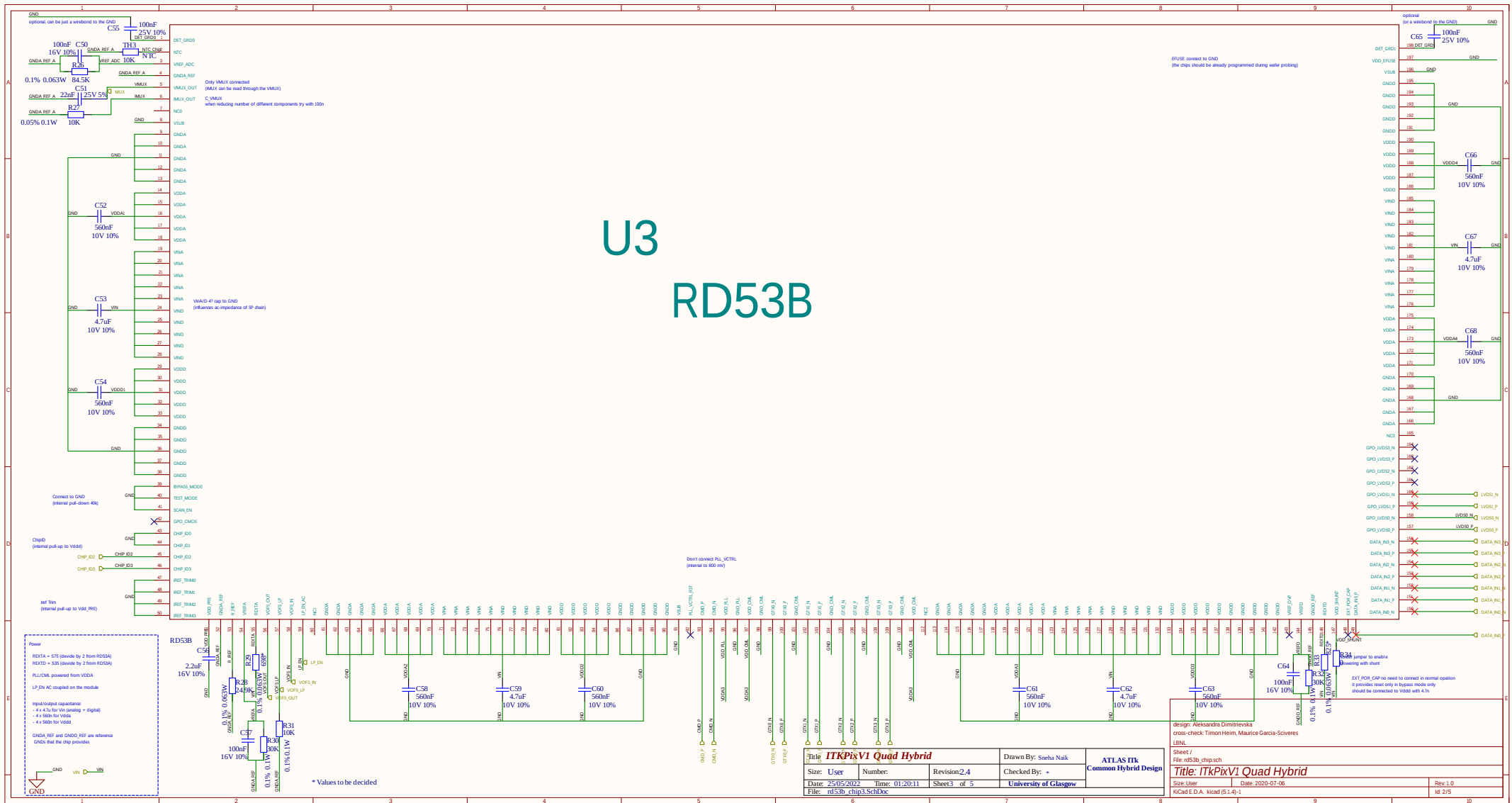
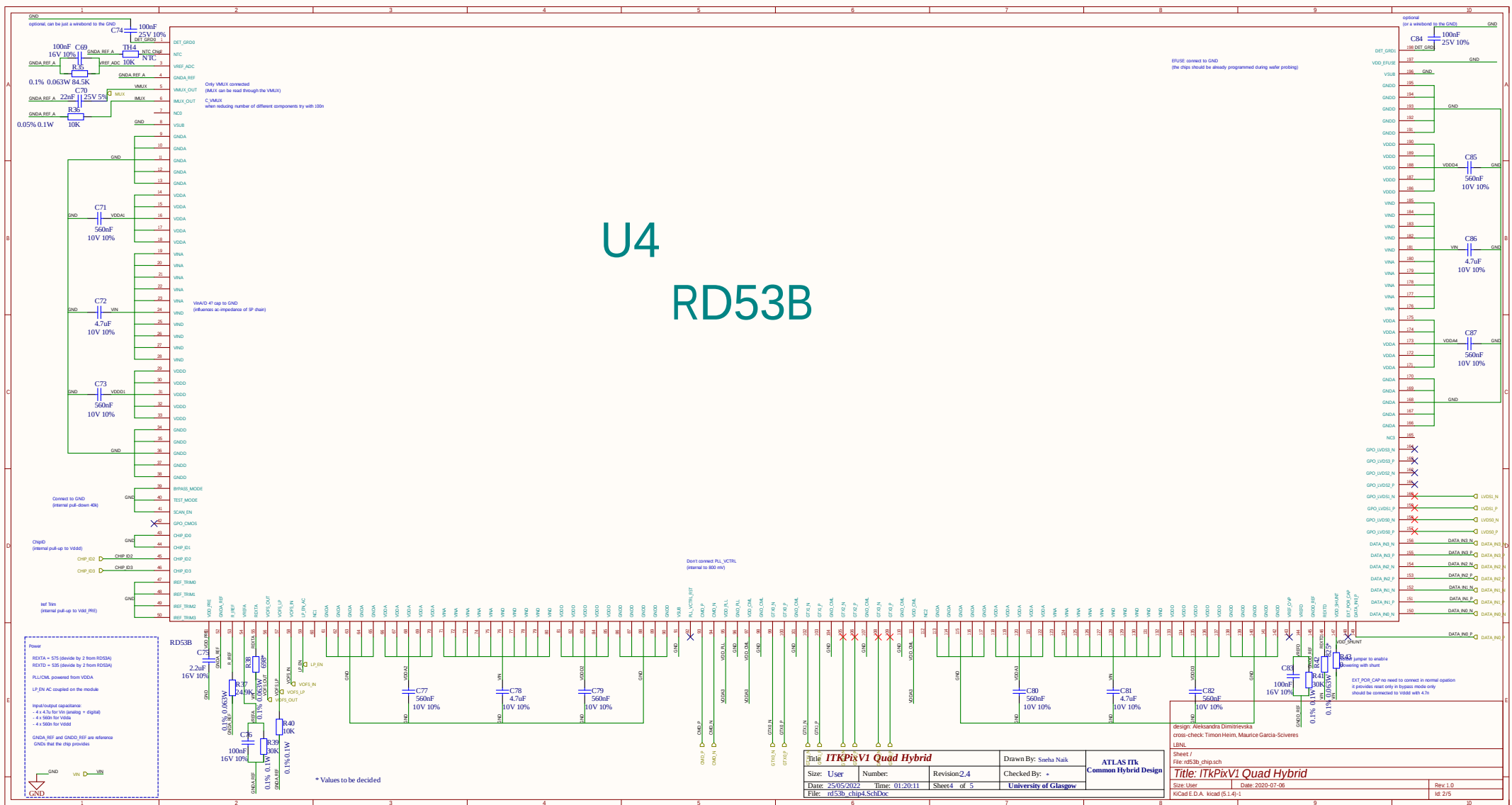






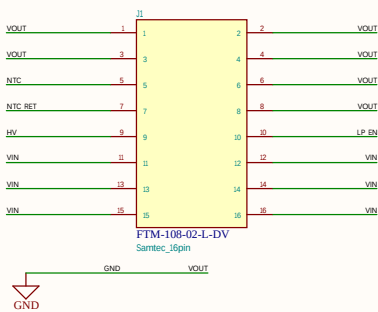
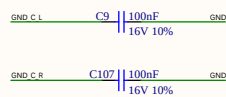
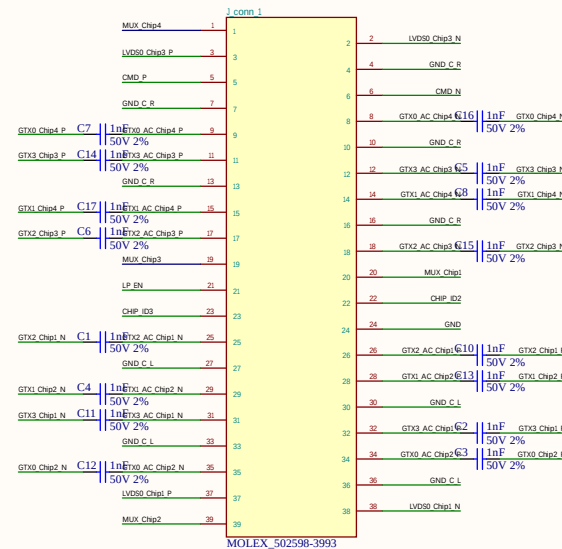
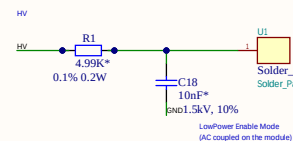
U3 RD53B



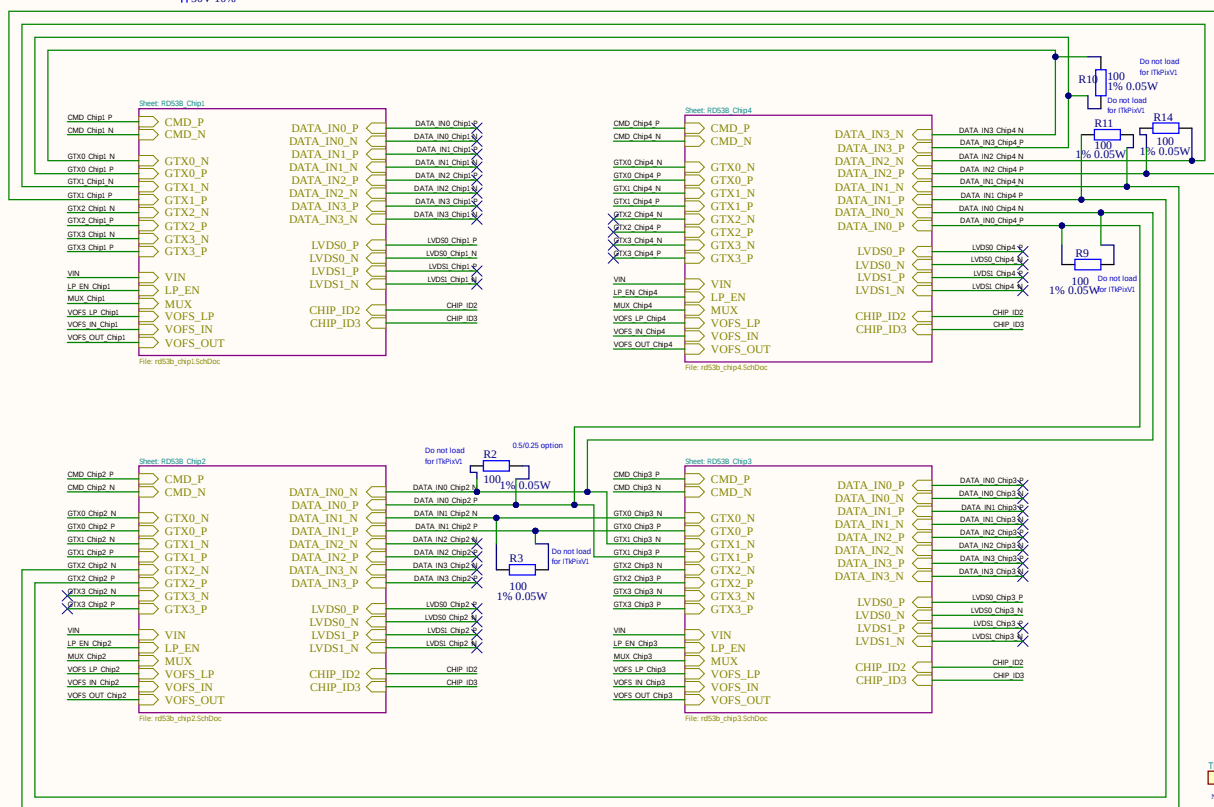
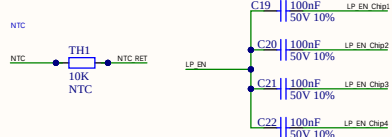


Quad

- Data sharing: 0.250 5/0.02.0 lines per FE
- (data_in lines have internal termination of 100 Ohms in ITkPixV1)
- If requested the next version of the chip the internal termination can be removed, then we need to terminate the data lines for which a additional resistors are added (do not load for ITkPixV1)
- CMD forwarding: from 2 chips
- Data pinout not final
- AC coupling capacitors on data lines are not final



* Values to be decided



design: Aleksandra Dimitrievska
cross-check: Timon Heim, Maurice Garcia-Silveres
LBNL

Sheet /
File: rd53b_quad_hybrid.sch

Title: ITkPixV1 Quad Hybrid

Size: User Date: 2020-07-06

KiCad E.D.A. KiCad (5.14)-1

Title <i>ITKPixV1 Quad Hybrid</i>			Drawn By: Sneha Naik	ATLAS ITk Common Hybrid Design
Size: User	Number:	Revision:2.4	Checked By: *	
Date: 25/05/2022	Time: 01:20:11	Sheet 5 of 5	University of Glasgow	

